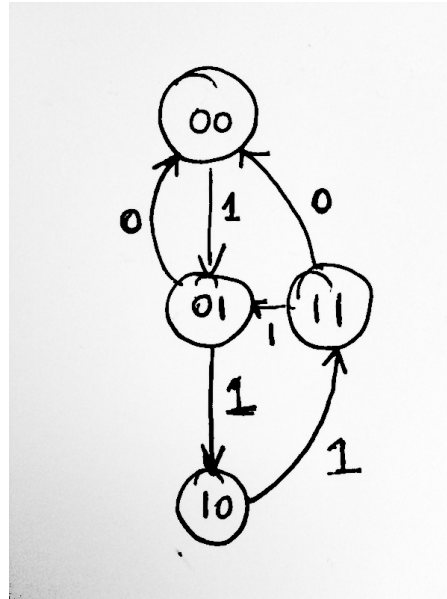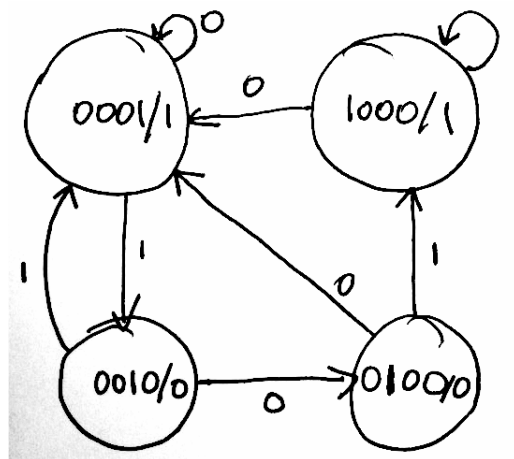# EECE256 Assignment 5

1. From the following state diagram, create the state table, and corresponding circuit using D flip-flops.
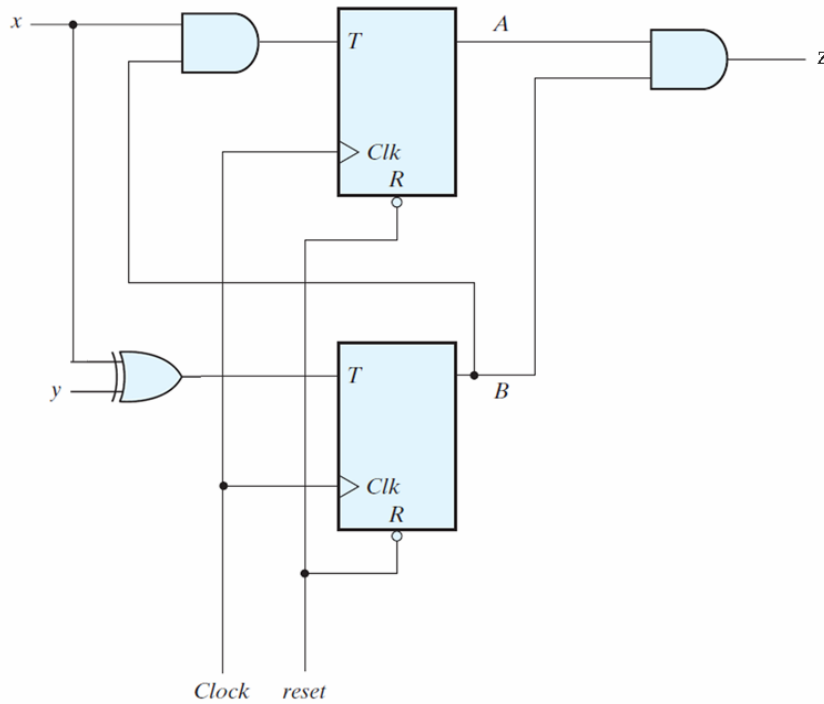

2. From the following state diagram, create the next state table, output, and flip flop excitation equations for a J/K flip-flop implementation using a 1-hot encoding.

3. Derive the state diagram for the following circuit. Transitions should be marked based on both inputs x and y, using the convention XY. Output Z should be encoded in the state.

4. Do Questions 5.11, 5.12, 5.23 and 5.30.