

1(b) - A V_T of around 0.25V is needed to maintain an acceptable I_{OFF}/I_{ON} ratio for digital logic.

2 - In CMOS, $V_T < V_{T0}$ because of short-channel effects, so V_{T0} must be made $> 0.25V$.

5 (a) $L_{tox} \downarrow$ to increase I_{ON} , & L to reduce size
 $x_j \downarrow$ to reduce SCE
 $N_A \uparrow$ counter the effect of C_{ox} on V_{T0} - i.e. keep it $> 0.25V$.

(c)
$$V_{T0} = V_{fb} + 2\phi_B + \gamma \sqrt{2\phi_B}$$

$$V_{fb} = \bar{\Phi}_G - \bar{\Phi}_S$$

1
$$2\phi_B = 2V_T \ln \frac{N_A}{n_i} = 2V_T \ln 10^8 = 0.954V$$

1
$$\gamma = \frac{1}{C_{ox}} \sqrt{\frac{2q\epsilon_s N_A}{r}} = \frac{8 \times 10^{-7}}{16 \times 8.85 \times 10^{-14}} \sqrt{2 \times 1.6 \times 10^{-19} \times 11.9 \times 8.85 \times 10^{-14} \times 10^{18}}$$

$$= 0.328V^{1/2}$$

$$\gamma \sqrt{2\phi_B} = 0.320V$$

$$\therefore V_{fb} = 0.45 - 0.954 + 0.320 = -0.824V = \bar{\Phi}_G - \bar{\Phi}_S$$

1
$$\therefore \bar{\Phi}_G = -0.824 + 5.144 = \underline{\underline{4.32 eV}}$$

2
$$\left\{ \begin{aligned} \bar{\Phi}_S &= \chi + (E_C - E_F) & E_C - E_F &= kT \ln \frac{N_C N_A}{n_i^2} = 1.044V \\ \bar{\Phi}_S &= 4.1 + 1.044 = 5.144V \end{aligned} \right.$$

2a) $n(y) = n_i e^{\frac{E_{FB} - E_{Fi}(y)}{kT}}$ is correct

$n(B) = n_i e^{\frac{E_{FB} - E_{Fi}(B)}{kT}} = n_i^2 / N_A$

$q\phi_B = E_{Fi}(B) - E_{FB}$

$\therefore n(y) = n_i \exp \frac{1}{kT} [E_{FB} - E_{Fi}(B) + E_{Fi}(B) - E_{Fi}(y)]$

4 $= n_i \exp \frac{1}{kT} [-q\phi_B + q\phi]$

b) $\frac{1}{m_{e,conv}^*} = \frac{1}{6} \left[\frac{2}{m_0^*} + \frac{4}{m_F^*} \right]$ unstrained based on equal probs. of occupancy of each valley.

On straining

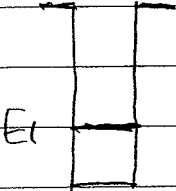
$\frac{1}{m_{e,conv}^*} = \frac{0.1}{m_0^*} + \frac{0.1}{m_F^*} + \frac{0.8}{m_F^*}$
 $\Delta_4 \quad \Delta_2$

$\rightarrow m_{e,conv}^* (\text{unstrained}) = 0.26$

$m_{e,conv}^* (\text{strained}) = 0.206$

4 $\mu \propto 1/\alpha \quad \therefore \% \text{ change} = \frac{\frac{1}{0.206} - \frac{1}{0.26}}{\frac{1}{0.26}} \rightarrow \underline{\underline{26\%}}$

c) $E_1 = \frac{\hbar^2 \pi^2}{2m^* a^2}$

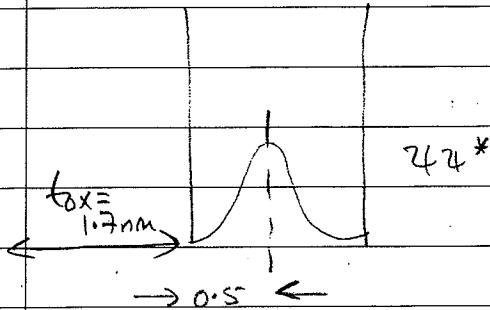


Take $m^* = 1 m_0$

2c) continued.

$$a = \sqrt{\frac{(6.63 \times 10^{-34})^2 \pi^2}{4\pi^2 \cdot 2 \times 1 \times 9.11 \times 10^{-31} \times 0.377 \times 1.6 \times 10^{-19}}}$$

$$= \underline{1 \text{ nm}}$$



$\epsilon_r = 3.9$	$\epsilon_r = 11.9$
$t = 1.7 \text{ nm}$	$t = 0.5 \text{ nm}$

$$C_{ox} = \left(\frac{1}{C_1} + \frac{1}{C_2} \right)^{-1} = \left(\frac{1.7 \times 10^{-9}}{3.9 \epsilon_0} + \frac{0.5 \times 10^{-9}}{11.9 \epsilon_0} \right)^{-1} = \epsilon_0 \times 10^9 \left(\frac{1.7}{3.9} + \frac{0.5}{11.9} \right)^{-1}$$

$$= \epsilon_0 \times 10^9 \times 2.09 = \underline{1.85 \times 10^{-2} \text{ F/m}^2}$$

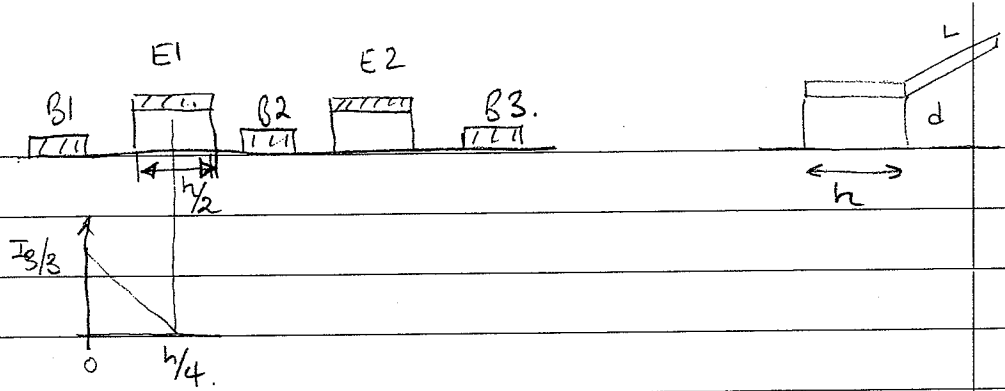
Without taking QM effects into account,

$$C_{ox} = \epsilon_0 \frac{3.9}{1.7 \times 10^{-9}} = 2.03 \times 10^{-2} \text{ F/m}^2$$

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$$\therefore \% \text{ change} = \frac{1.85 - 2.03}{2.03} \times 100 = \underline{-8.8\%}$$

3 a)



Originally $P_B^0 = \frac{1}{3} I_B^2 \frac{\rho h}{A}$

$$N_{B0} = 3 \times \frac{1}{3} \left(\frac{I_B}{3}\right)^2 \frac{\rho h}{4A}$$

$$= 3 \times \frac{1}{3} \frac{I_B^2}{9} \frac{\rho h}{A} \cdot \frac{1}{4}$$

$$= P_B^0 \frac{1}{12}$$

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b) $g_m = \frac{\partial I_c}{\partial V_{BE}} = \frac{I_c}{V_{th}} = \frac{6.5 \times 10^{-2} \times (0.1) \times (0.1)}{0.0259} = \underline{\underline{251 \text{ S}}}$

4 $g_{\pi} = \frac{\partial I_B}{\partial V_{BE}} \approx \frac{I_{B,rc}}{V_{th}} = \frac{0.82 \times 10^{-2}}{0.0259} = \underline{\underline{0.124 \text{ S}}}$

c) From (11.11) $|i_c|_{\omega \rightarrow 0}^2 = g_m^2 v_{be}^2$

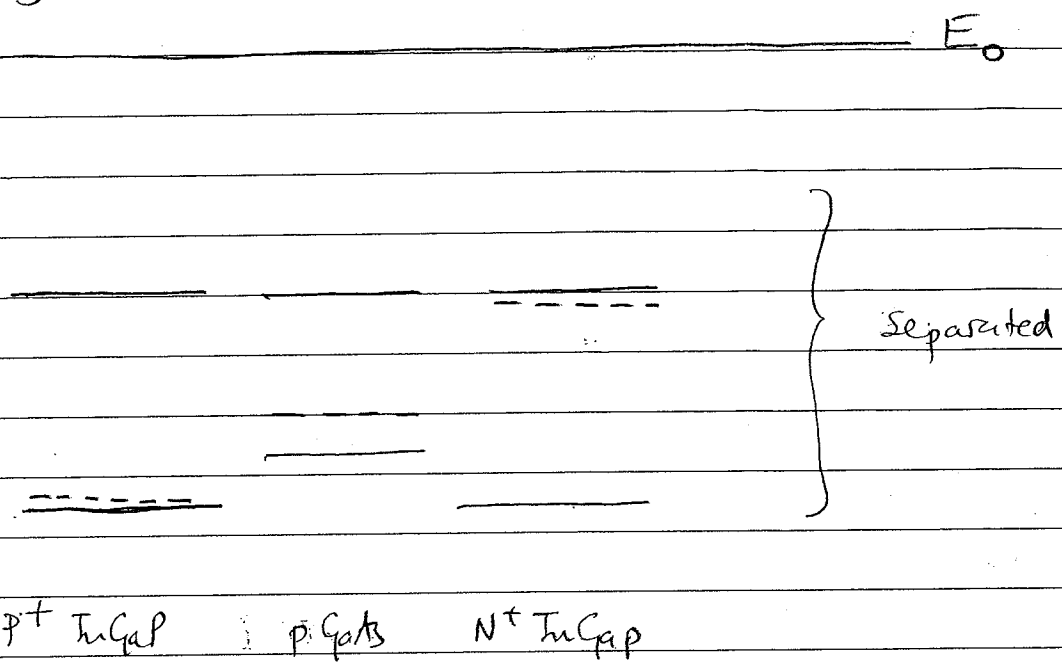
From (11.13) $|i_b|_{\omega \rightarrow 0}^2 = g_{\pi}^2 v_{be}^2$

3 $\therefore \left| \frac{i_c}{i_b} \right|_{\omega \rightarrow 0}^2 = \left(\frac{g_m}{g_{\pi}} \right)^2$

$$\chi_{\text{InGaP}} = \chi_{\text{GaAs}} = 4.07 \text{ eV}$$

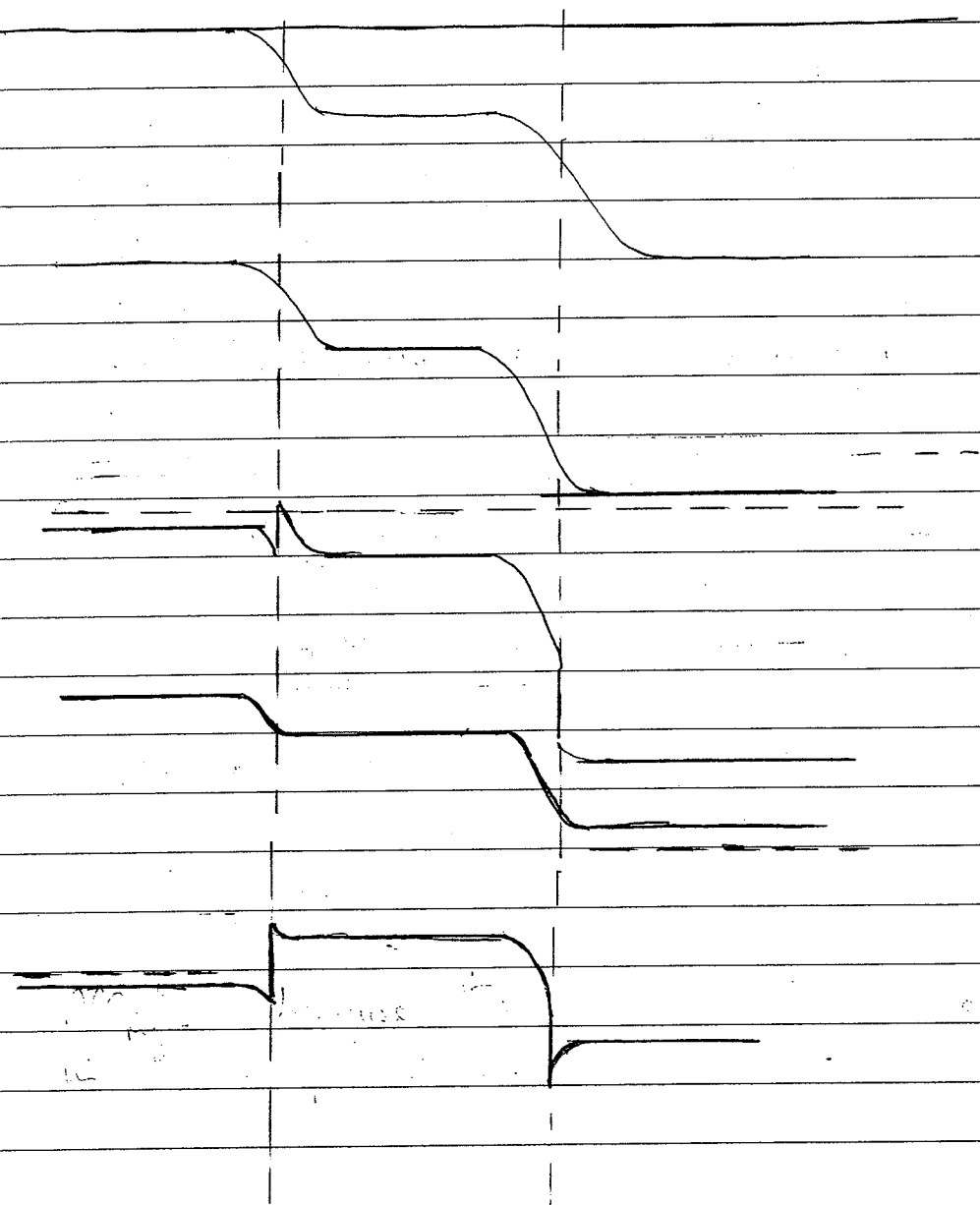
$$E_{g \text{ InGaP}} = 1.82 \text{ eV}, \quad E_{g \text{ GaAs}} = 1.42 \text{ eV}$$

4a)



5

b)



4b) Good "well" for holes, but not for electrons.

3 \therefore AlGaAs LED will give higher η_c .

4c) (i)
$$\text{WPE} = \frac{S_{\text{out}}}{P_{\text{in}}} = \frac{1 \times 10^{-3}}{1 \times 10^{-3} \times 2} \times 100 = 50\%$$

For both LEDs

(ii) Luminous efficacy =
$$\frac{\Phi}{S'_{\text{out}}} = 683 \frac{\text{lm}}{\text{W}} \frac{S'_{\text{out}}}{S'_{\text{out}}}$$
 for monochromatic

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$$= 683 \times 10^{-1} = \underline{68.3 \text{ lm/W for B}}$$

$$= \underline{0 \text{ for A}}$$

(iii) Luminous efficacy =
$$\frac{\Phi}{P_{\text{in}}} = \frac{68.3 \times 1 \times 10^{-3}}{1 \times 10^{-3} \times 2}$$

$$= \underline{34.15 \text{ lm/W for B}}$$

$$= \underline{0 \text{ for A}}$$

5(a) We need $V_T = 1.5V$

$$\therefore \Delta V_T = +0.5V$$

$$\Delta V_T = -\Delta \phi_F \frac{t_{ox}}{\epsilon_{ox}} = qN \frac{t_{ox}}{\epsilon_{ox}}$$

$$\therefore N = \frac{\Delta V_T \epsilon_0}{q \epsilon_{ox}} = \frac{0.5}{1.6 \times 10^{-19}} \times \frac{3.9 \times 8.85 \times 10^{-12}}{20 \times 10^{-9}} / m^2$$

$$= 5.39 \times 10^{15} / m^2$$

$$= 5.39 \times 10^{15} \times 10^{-14} = \underline{\underline{53.9 \text{ electrons}}}$$

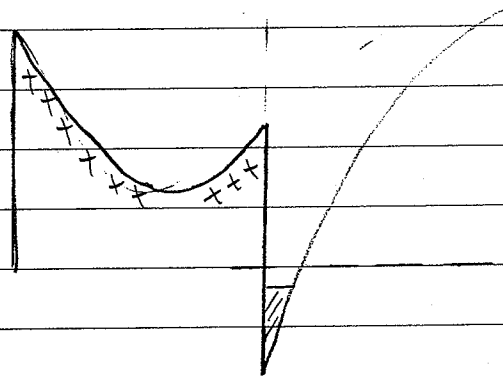
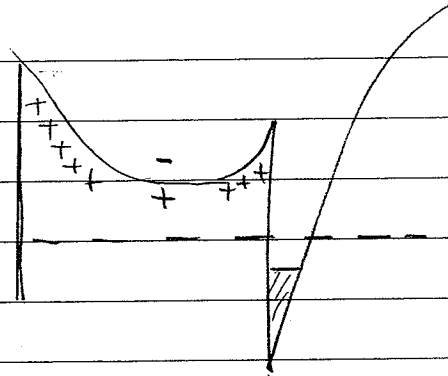
5(b) From Assignment 5 $I_{off} (\text{CMOS65}) = 9 \times 10^{-12} \text{ A}/\mu\text{m}$

$$\therefore I_{off} = 9 \times 10^{-12} \times 0.1 = \underline{\underline{9 \times 10^{-13} \text{ A}}}$$

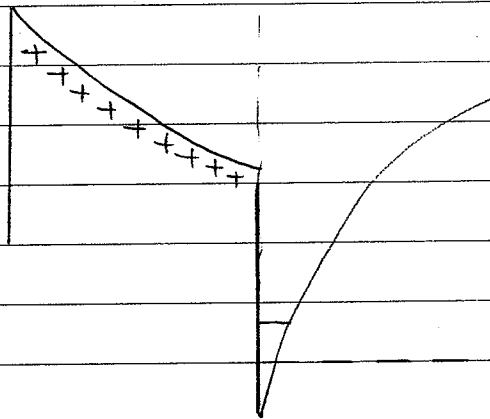
$$C = \frac{Q}{V} \quad Q = 1 \times 10^{-12} \times 1 = 10^{-12} \text{ C}$$

$$\therefore t_{50\%} = \frac{0.5 \times 10^{-12}}{9 \times 10^{-13}} = \underline{\underline{\frac{5}{9} \text{ sec}}}$$

5c) (i)



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2 d) A more -ve V_{gs} is required to reach threshold in this case.

\therefore HJFET A has the more +ve threshold voltage.